

**Amendments to the Specification:**

Please replace the title with the following amended title:

VOLTAGE REGULATOR CIRCUIT ARRANGEMENT FOR GENERATING  
AN OUTPUT VOLTAGE IN DEPENDENCE OF A REFERENCE SIGNAL

Please insert the following abstract:

The invention relates to a voltage regulator circuit arrangement comprising a voltage regulator for generating an out-put voltage in dependence of a reference signal, characterized in that a reference signal generation circuit is provided for generating said reference signal comprising a plurality of inputs connected to internal terminals, whereby a sub-set of said plurality internal terminals is connected to an external terminal.